**EECS 392: FPGA Systems Design Projects – Project Plan**

*Team Name*

*Team Members*

* Jason Arkin
* Sebastian Rodriguez
* Spencer Williams
* Sebastian Witkowski

*Project Title*

J-SSSynth

*Project Description / Overview*

System on-board synthesizer that implements synth and volume control and graphics user interface.

*System Architecture / IO / Peripherals / Memory*

*Block Diagram*

*Tasks / Gantt Chart*

*Members Responsibilities*

* VGA / GUI Output Module – Rodriguez
* Synthetizer Module – Arkin, Rodriguez, Williams, Witkowski
* Input / Output Module – Rodriguez, Witkowski

*Development Plan*

*Deliverables*

*Project Costs*